

Figure 1

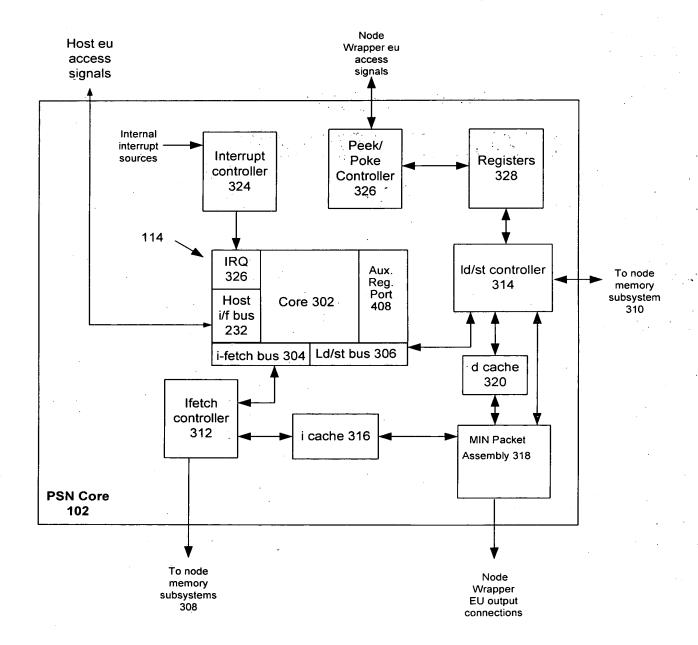


Figure 3

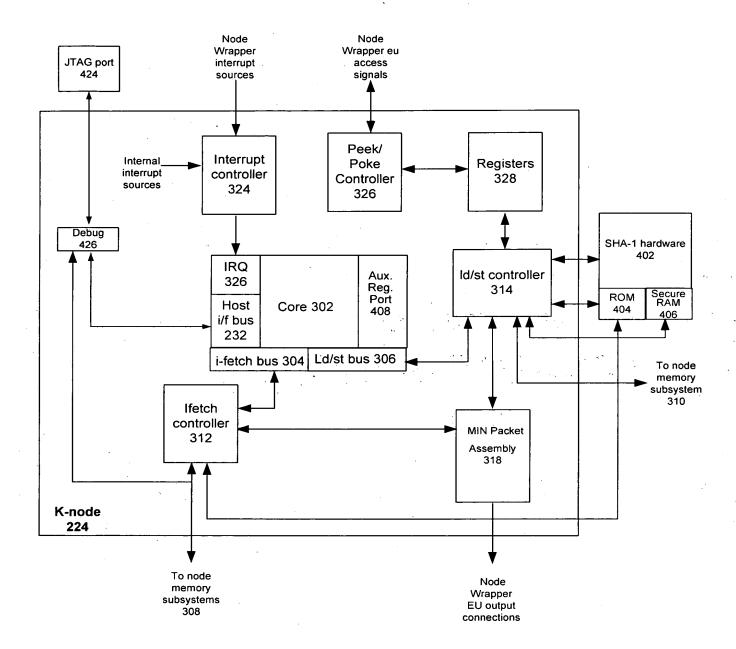


Figure 4

Data Memory Map	Instruction Memory Map								
	ì	0×0000	0000	F		Reserved		7	0×000 0000 0×0000 000
Node Memory					٨	lode Memo	ry		
·		0×0000	4000			of a complete contract		I	0×000 4000
Reserved		0×0000	8000	.	. <u>171</u>	Reserved	the s	-,-	0×000 8000
ROM						ROM			
Reserved		0 x 0 0 0 0	A000			Reserved		·	0×000 A000
		0 x 0 0 0 0	C000		Service Andreas		<u> </u>	<u>*</u> '	0×200 0000 0×0000 000
Secure Ram				;		ernal Instru emory Over			
Reserved		0 x 0 0 0 0	CACO	L					0x3ff ffff 0x01ff fff
		0 x 0 0 0 1	8000						
Node Registers						٠ .			
Reserved		0 x 0 0 0 1		Not					Overaly and
Input/Output Buffer		0 × 0 0 4 0	0000			xternal Data emory loca			the same ernal memory.
Access			•						
Forward/Backward		0 x 0 0 6 0	0000						•
Acknowledgement									
Own Node Wrapper	·	0×0070	0000						
Access									
Reserved		0x0070							
External Instruction		0×0200	0000	: 0×00'00	0000	(K-Node	addr	: XMC	addr)
Memory Overlay						(K-Node		V W C	a dd u V
Reserved				: 0×0200 : 0×0000		(K-Node			
External Data Memory		021000	0000	. 02000	, 0000				•
		0×2000	0000	: 0×100	0 0000	(K-Node	addr :	XMC	addr)
Reserved				: 0×0000		(K-Node	addr :	XMC	addr)
Chip 0 External Data Memory						,			
Caternal Data Memory		0×5000	0000	: 0×000	0000	(K-Noḋe	addr :	хмс	addr)
Chip1 External Data Memory									
External Data Memory		0×6000	0000	: 0×000	0 0000	(K - N o d e	addr	: хмс	addr)
Chip 2 External Data Memory									
		0 x 7 0 0 0	0000	: 0 × 0 0 0	0 0000	(K-Node	addr	: хмс	addr)
Chip 3 External Data Memory									
<u> </u>		0 × 8 0 0 C	0000	: 0 x 1 0 0	0 0000	(K-Node	addr :	хмс	addr)
,									
ACM Chip and	Ì								
Node Access (Details in Figure 2)									
Reserved .	1	0 x 8 3 4 0	0000						

Figure 5A

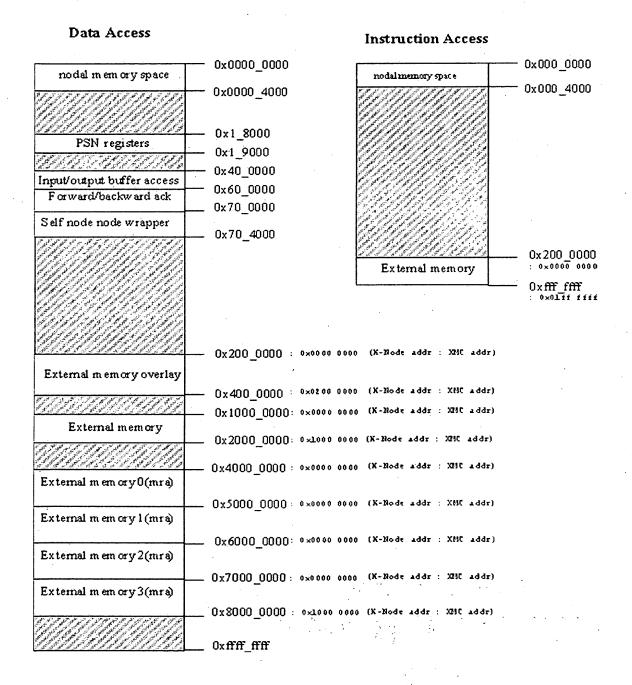


Figure 5B

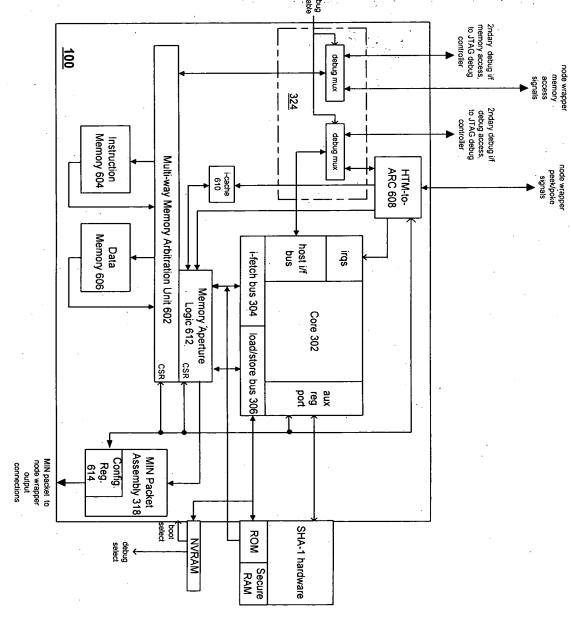


Figure 6

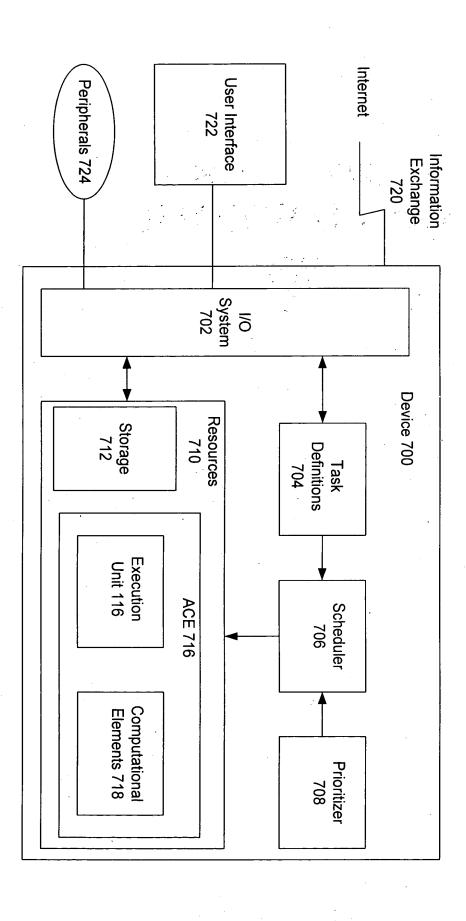


Figure 7

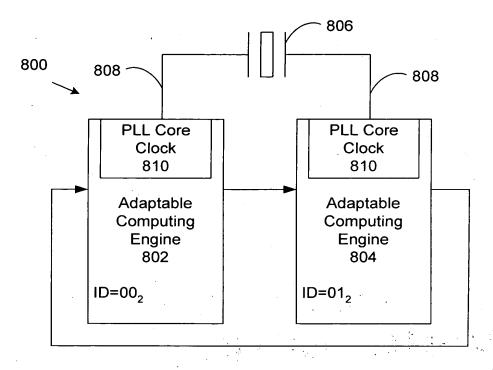


Figure 8A

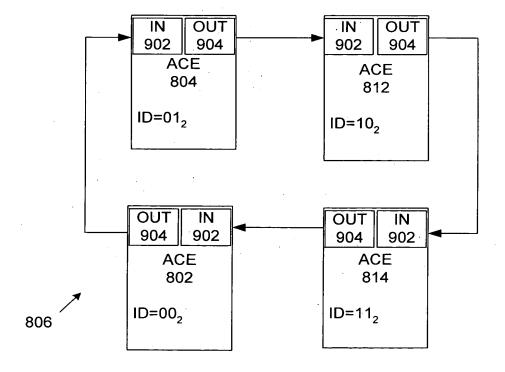


Figure 8B

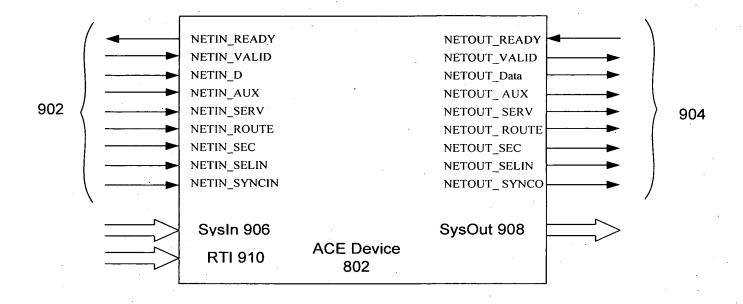


Figure 9

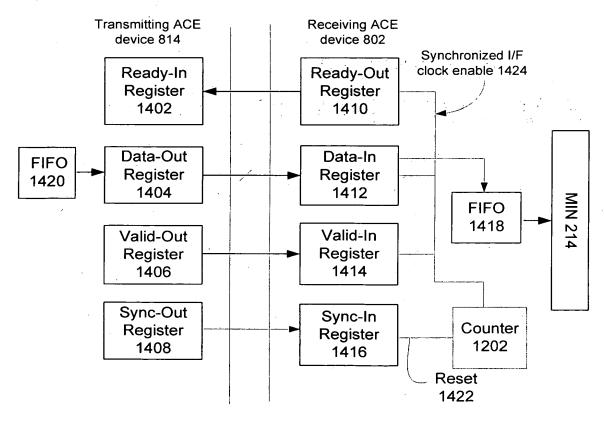


Figure 14

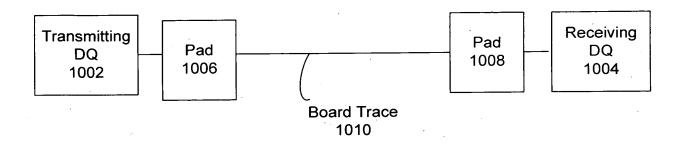


Figure 10

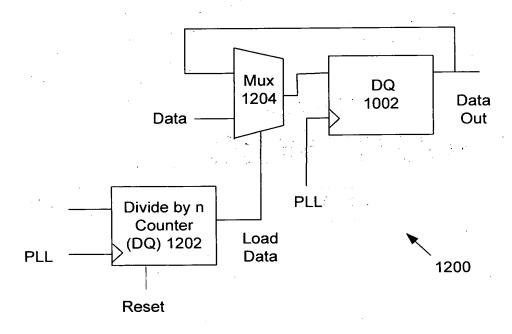


Figure 12

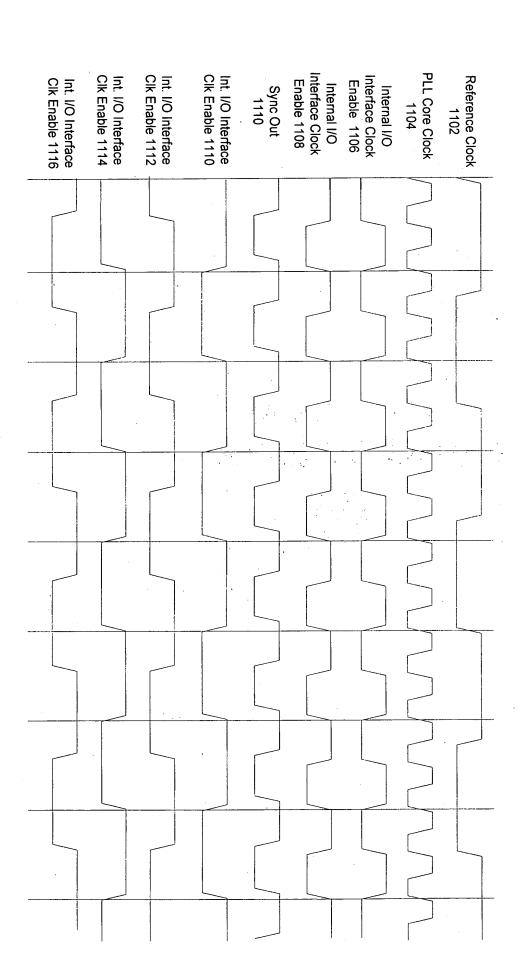


Figure 11

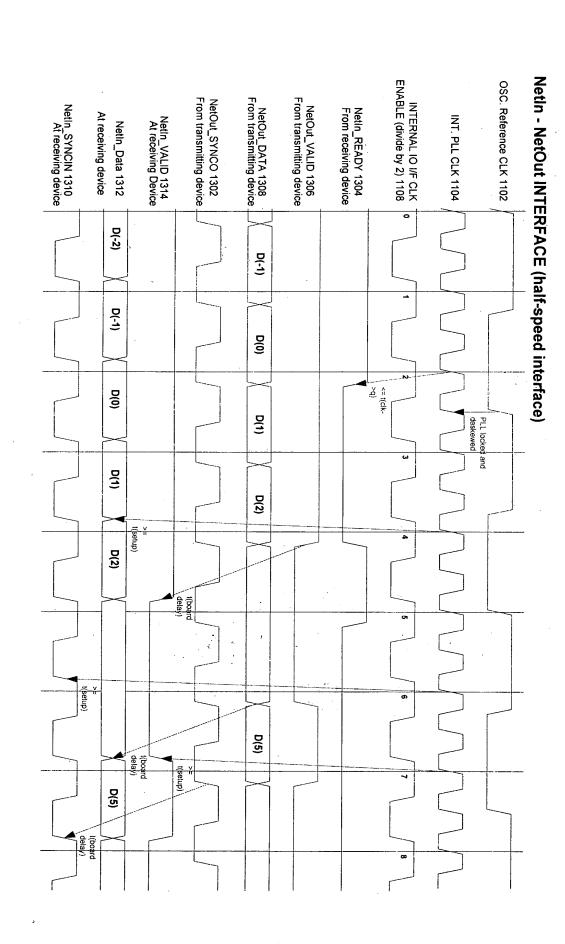


Figure 13A

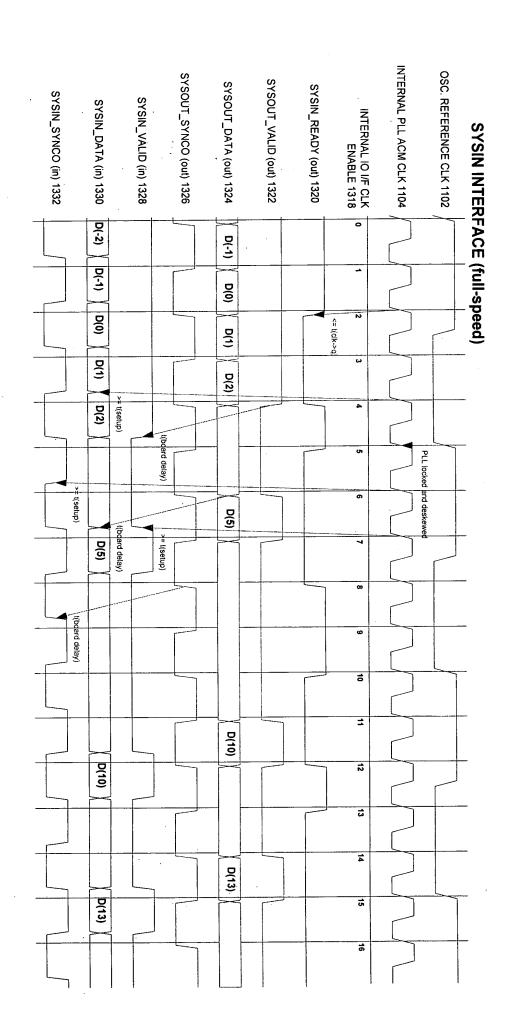


Figure 13B

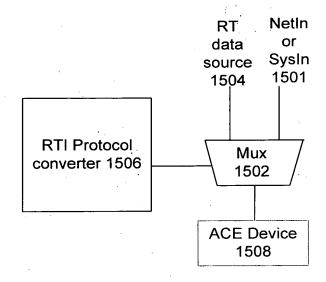


Figure 15

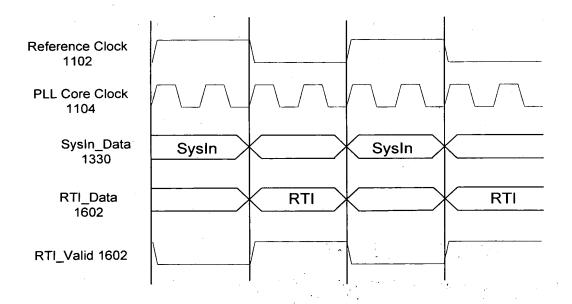


Figure 16